

#### Features

- 650 V, 40 A, Low Collector-Emitter Saturation Voltage (V<sub>CE(sat)</sub>)
- Novel trench-gate field-stop technology
- Optimized for conduction -
- High-speed switching
- Maximum operating T<sub>i</sub> = 175 °C
- RoHS compliant\*

#### Applications

- Switched-Mode Power Supplies (SMPS)
- Uninterruptible Power Sources (UPS)
- Power Factor Correction (PFC)
- Inverters

BIDW40N65H5 Insulated Gate Bipolar Transistor (IGBT)

- Welding converters
- Photovoltaic

# **General Information**

The Bourns® Model BIDW40N65H5 IGBT device combines technology from a MOS gate and a bipolar transistor, resulting in an optimum component for high voltage and high current applications. This device uses Trench-Gate Field-Stop technology providing greater control of dynamic characteristics while resulting in a lower Collector-Emitter Saturation Voltage (V<sub>CE(sat)</sub>) and fewer switching losses.

#### **Additional Information**

Click these links for more information:



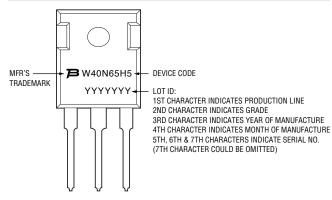
#### Maximum Electrical Ratings (T<sub>C</sub> = 25 °C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CES</sub>	650	V
Continuous Collector Current ( $T_C = 25 \text{ °C}$ ), limited by $T_{jmax}$	Ι <sub>C</sub>	80	А
Continuous Collector Current (T <sub>C</sub> = 100 °C), limited by $T_{jmax}$	Ι <sub>C</sub>	40	А
Pulsed Collector Current, tp limited by Tjmax	I <sub>CP</sub>	120	А
Gate-Emitter Voltage	V <sub>GE</sub>	±20	V
Gate-Emitter Voltage (t <sub>p</sub> ≤10 $\mu$ s, D < 1 %)	V <sub>GE</sub>	±30	V
Continuous Forward Current (T <sub>C</sub> = 100 °C), limited by $T_{jmax}$	l <sub>F</sub>	20	А
Total Power Dissipation	P <sub>total</sub>	300	W
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C
Operating Junction Temperature	Tj	-40 to +175	°C

#### **Thermal Resistance**

Parameter	Symbol	Мах	Unit
IGBT Thermal Resistance Junction - Case	R <sub>th(j-c)_IGBT</sub>	0.5	°C/W
Diode Thermal Resistance Junction - Case	R <sub>th(j-c)_Diode</sub>	1.4	°C/W

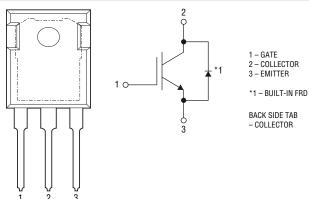
#### **Typical Part Marking**



WARNING Cancer and

**Reproductive Harm** 

#### **Internal Circuit**



\*RoHS Directive 2015/863, Mar 31, 2015 and Annex.

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Users should verify actual device performance in their specific applications.

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#### Static Electrical Characteristics (T<sub>C</sub> = 25 °C, Unless Otherwise Specified)

Parameter	Symbol	Conditions	Value			Unit
Parameter			Min.	Тур.	Max.	Unit
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>	$V_{GE}$ = 0 V, $I_C$ = 250 $\mu$ A	650	_	—	V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A, T <sub>C</sub> = 25 °C	_	1.65	2.1	v
		$V_{GE}$ = 15 V, I <sub>C</sub> = 40 A, T <sub>C</sub> = 150 °C	_	1.85	_	
Diada Famuard On Valtage	N	I <sub>F</sub> = 20 A, T <sub>C</sub> = 25 °C	_	1.5	2	V
Diode Forward On-Voltage	V <sub>F</sub>	I <sub>F</sub> = 20 A, T <sub>C</sub> = 150 °C	_	1.4	_	V
Gate Threshold Voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 250 \ \mu A$	3.2	4.5	5.8	V
Collector Cut-off Current	I <sub>CES</sub>	$V_{GE} = 0 V, V_{CE} = 650 V$	_	_	200	μA
Gate-Emitter Leakage Current	I <sub>GES</sub>	$V_{CE}$ = 0 V, $V_{GE}$ = ± 20 V	_	_	±400	nA

#### Dynamic Electrical Characteristics (T<sub>C</sub> = 25 °C, Unless Otherwise Specified)

Parameter Sym	0h.al	Symbol Conditions	Value			11-24
	Symbol		Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V, f = 1 MHz	_	3150	-	
Output Capacitance	C <sub>oes</sub>		_	63	-	pF
Reverse Transfer Capacitance	C <sub>res</sub>		_	11	_	
Total Gate Charge	Qg		_	111	_	
Gate-Emitter Charge	Q <sub>ge</sub>	$V_{CE} = 400 \text{ V}, V_{GE} = 15 \text{ V},$ $I_{C} = 40.0 \text{ A}$	_	29	_	nC
Gate-Collector Charge	Q <sub>gc</sub>		_	25	_	

#### IGBT Switching Characteristics (Inductive Load, T<sub>C</sub> = 25 °C, unless otherwise specified)

Parameter	Gumbal	Conditions	Value			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Turn-on Delay Time	t <sub>d(on)</sub>		_	28	_	ns
Current Rise Time	t <sub>r</sub>	$V_{CE} = 400 \text{ V}, \text{ V}_{GE} = 15 \text{ V},$ $I_{C} = 40.0 \text{ A}, \text{ R}_{G} = 10 \Omega$	_	80	_	ns
Turn-off Delay Time	t <sub>d(off)</sub>		_	116	_	ns
Current Fall Time	t <sub>f</sub>		_	98	_	ns
Turn-on Switching Energy	Eon		_	1.9	_	mJ
Turn-off Switching Energy	E <sub>off</sub>		_	0.52	_	mJ
Total Switching Energy	E <sub>ts</sub>		—	2.4	_	mJ

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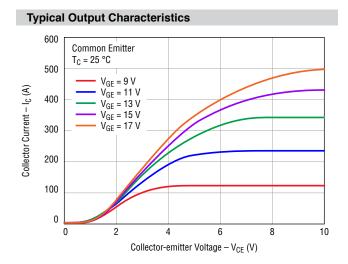
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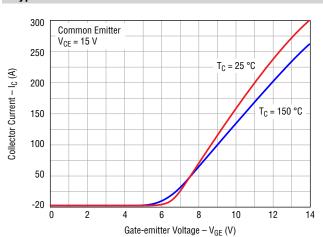
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#### Diode Switching Characteristics (T<sub>C</sub> = 25 °C, unless otherwise specified)

Devemeter	ter Cumbel Conditions		Value			Unit
Parameter Symb	Symbol	Conditions	Min.	Тур.	Max.	Unit
Reverse Recovery Time	t <sub>rr</sub>	dl <sub>F</sub> /dt = 200 A/µs,	_	165	_	ns
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 20.0 A	_	223	-	nC

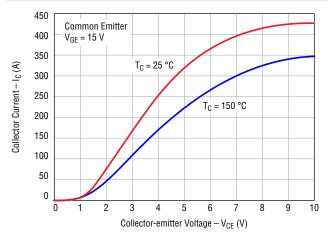
#### **Electrical Characteristic Performance**



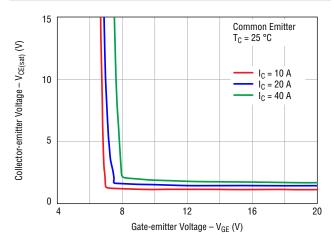


Typical Transfer Characteristics

**Typical Saturation Voltage Characteristics** 



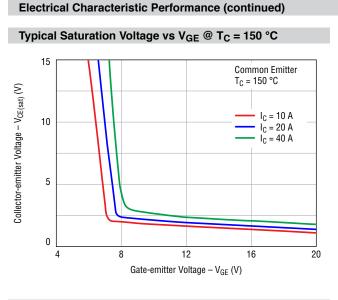
#### Typical Saturation Voltage vs V<sub>GE</sub> @ T<sub>C</sub> = 25 °C

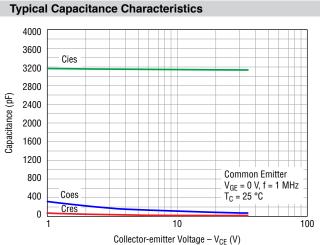


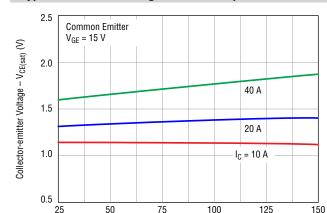
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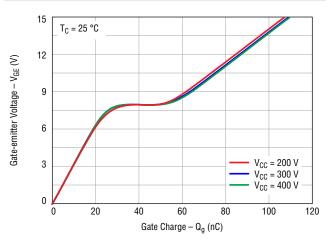




Case Temperature - T<sub>C</sub> (°C)

#### **Typical Saturation Voltage vs Case Temperature**

**Typical Gate Charge Characteristics** 



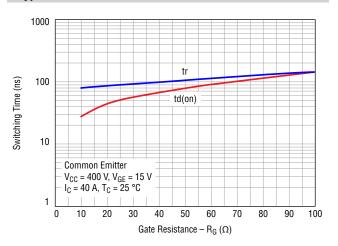
Specifications are subject to change without notice.

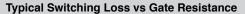
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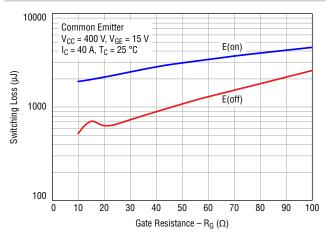
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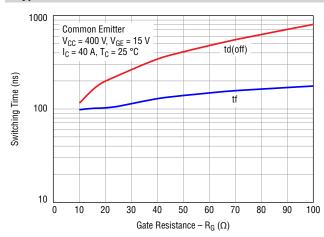
**Electrical Characteristic Performance (continued)** 

#### Typical Turn-on Characteristics vs Gate Resistance



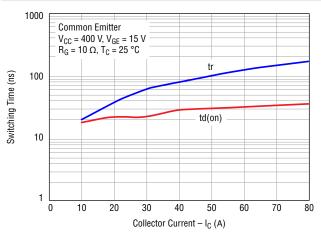






#### Typical Turn-off Characteristics vs Gate Resistance

Typical Turn-on Characteristics vs Collector Current



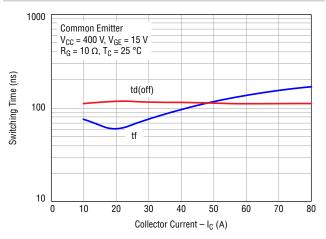
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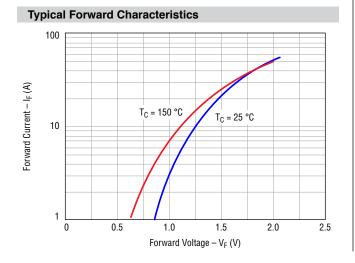
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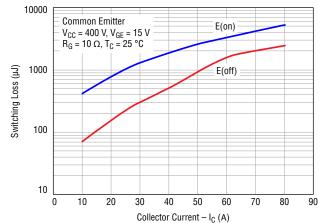
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#### **Electrical Characteristic Performance (continued)**

#### **Typical Turn-off Characteristics vs Collector Current**

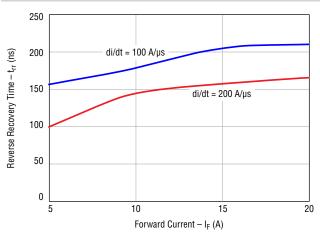






# Typical Switching Loss Characteristics vs Collector Current

#### **Typical Reverse Recovery Time vs Forward Current**



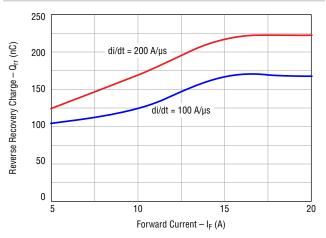
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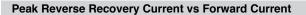
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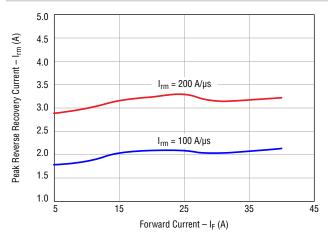
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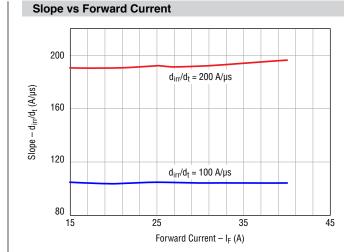
#### **Electrical Characteristic Performance (continued)**

#### **Typical Reverse Recovery Charge vs Forward Current**

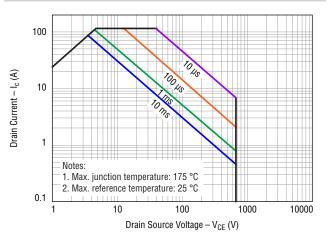








#### Forward Bias Safe Operating Area



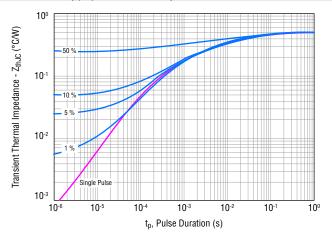
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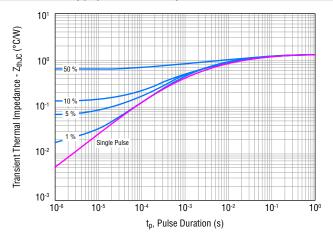
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**Electrical Characteristic Performance (continued)** 

IGBT Transient Thermal Impedance vs tp(on) Duration (D=tp/T)



#### Diode Transient Thermal Impedance vs $t_{p(on)}$ Duration (D=t\_p/T)



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# Inductive Load Test Circuit

L = 200  $\mu H,\,V_{CE}$  = 400 V,  $V_{GE}$  = 15 V,  $I_{C}$  = 40 A,  $R_{G}$  = 10  $\Omega$ 

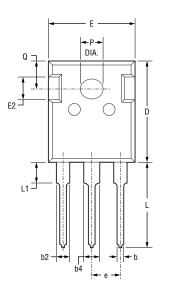
#### B I D W 40 N 65 H5 B = Bourns<sup>®</sup> -I = IGBT -Туре D = Discrete Package Code -W = TO-247-3L Current Rating 40 = 40 A Device Type -N = N-channel Nominal Voltage (divided by 10) -65 = 650 V Optimization -H = High Speed Version Number 5 = Revision Control

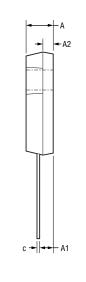
Environmental Characteristics
ESD Class (HBM)2

# est Circuit How to Order

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#### **Product Dimensions**





#### Packaging Specifications

BIDW40N65H5 ...... 30 pieces per tube

Symbol	Min.	Nom.	Max.		
А	<u>4.80</u> (.189)	<u>5.00</u> (.197)	<u>5.20</u> (.205)		
A1	<u>2.21</u> (.087)	<u>2.41</u> (.095)	<u>2.59</u> (.102)		
A2	<u>1.85</u> (.073)	<u>2.00</u> (.079)	<u>2.15</u> (.085)		
b	<u>1.11</u> (.044)	_	<u>1.36</u> (.054)		
b2	<u>1.91</u> (.075)	_	<u>2.25</u> (.089)		
b4	<u>2.91</u> (.115)	-	<u>3.25</u> (.128)		
с	<u>0.51</u> (.020)	_	<u>0.75</u> (.030)		
D	<u>20.80</u> (.819)	<u>21.00</u> (.827)	<u>21.30</u> (.839)		
E	<u>15.50</u> (.610)	<u>15.80</u> (.622)	<u>16.10</u> (.634)		
E2	<u>4.40</u> (.173)	<u>5.00</u> (.197)	<u>5.20</u> (.205)		
е		5.44 (.214) BSC			
L	<u>19.72</u> (.776)	<u>19.92</u> (.784)	<u>20.22</u> (.796)		
L1	_	_	<u>4.30</u> (.169)		
Р	<u>3.40</u> (.134)	_	<u>3.80</u> (.150)		
Q	$\frac{5.60}{(.220)}$	$\frac{5.80}{(.228)}$	<u>6.00</u> (.236)		

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Asia-Pacific: Tel: +886-2 2562-4117 Email: <u>asiacus@bourns.com</u> EMEA: Tel: +36 88 885 877 Email: <u>eurocus@bourns.com</u> The Americas: Tel: +1-951 781-5500 Email: <u>americus@bourns.com</u>

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