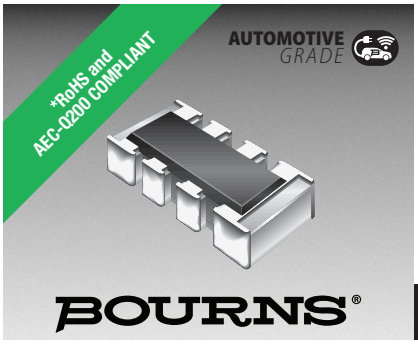


Features

- RoHS compliant*
- Convex terminal style
- 2/4 isolated elements available
- Resistance tolerance: 5 %
- Resistance range: 3 Ω to 1 MΩ and zero jumper
- AEC-Q200 compliant

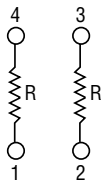
CAY10A-LF Series – Thick Film Chip Arrays

Electrical Characteristics

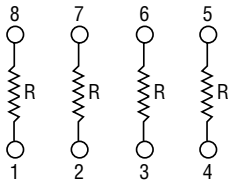
Characteristic	Model No.	
	CAY10A-xxxJ2LF	CAY10A-xxxJ4LF
Number of Elements (Isolated)	2	4
Power Rating @ 70 °C per Resistor	63 mW	
Resistor Tolerance	5 %	
Resistor Range & TCR (E24 for 5 %) plus zero ohm jumper	5 %, 10 ~ 1 MΩ ... 200 ppm/°C 5 %, 3 ~ 9, 1 Ω ... 500 ppm/°C	
Maximum Overload Voltage	50 V	
Maximum Working Voltage	25 V	
Operating Temperature Range	-55 to +125 °C	
Rating Temperature	+70 °C	
Packaging	10,000 pieces per reel	
Zero Ohm Jumper Current Rating / Max. Resistance (per element)	1 A / 2.5 A / 50 mΩ max.	

Isolated Circuit

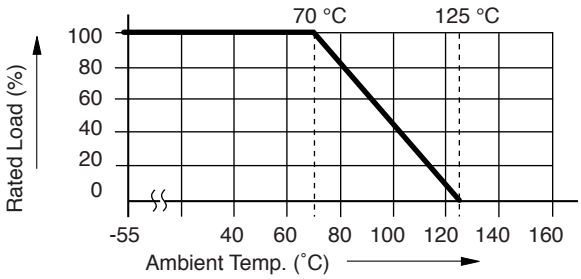
CAY10A-xxxx2LF



CAY10A-xxxx4LF



Derating Curve

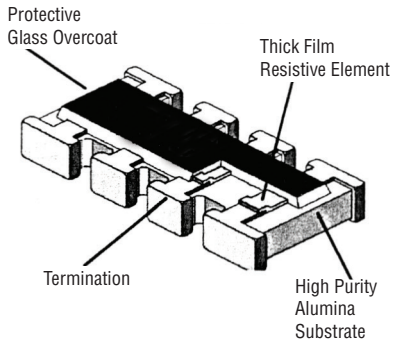


Additional Information

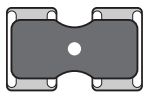
Click these links for more information:

PRODUCT | TECHNICAL LIBRARY | INVENTORY | SAMPLES | CONTACT

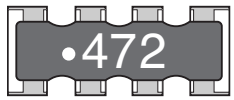
Construction



Typical Part Marking



CAY10A-x2LF
No part marking



CAY10A-x4LF ±5 % (E24)
3 digits; first two digits are significant, third digit is the number of zeroes to follow.

EX: 472 = 4700 Ω = 4.7K Ω
000 = 0 Ω

Storage Conditions

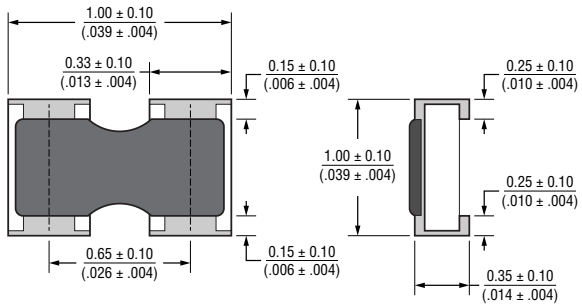
5~35 °C, 40~75 % RH, 2 years

WARNING Cancer and Reproductive Harm
www.P65Warnings.ca.gov

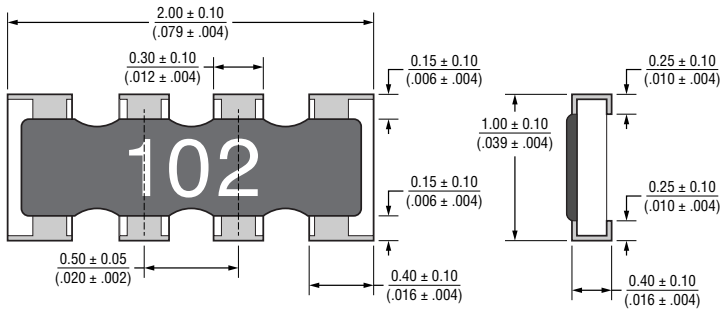
*RoHS Directive 2015/863, Mar 31, 2015 and Annex.
Specifications are subject to change without notice.
Users should verify actual device performance in their specific applications.

Product Dimensions

CAY10A-xxxJ2LF

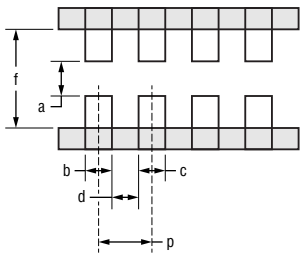


CAY10A-xxxJ4LF



DIMENSIONS: MM / (INCHES)

Recommended Pad Layout



Model	a	b	c	d	p	f
CAY10A-xxxJ2LF	0.5 (.020)	0.35 ~ 0.40 (.014 ~ .016)	0.35 ~ 0.40 (.014 ~ .016)	--	0.65 (.026)	1.4 ~ 1.5 (.055 ~ .059)
CAY10A-xxxJ4LF	0.4 (.016)	0.525 (.021)	0.25 (.010)	0.25 (.010)	--	1.4 (.055)

DIMENSIONS: MM / (INCHES)

How to Order

CA Y 10 A - 103 J 4 LF

Series
CA = Chip Array

Type
Y = Convex

Model
10 = 04 Package Width

Feature
A = AEC-Q200 Compliant

Resistance Code
For 5 % Tolerance: (E24)
<10 Ω – "R" represents decimal point (example 4R7 = 4.7 Ω)
≥10 Ω – First two digits are significant, third digit represents the number of zeroes to follow (example: 474 = 470k Ω)
000 = Zero Ohm Jumper.

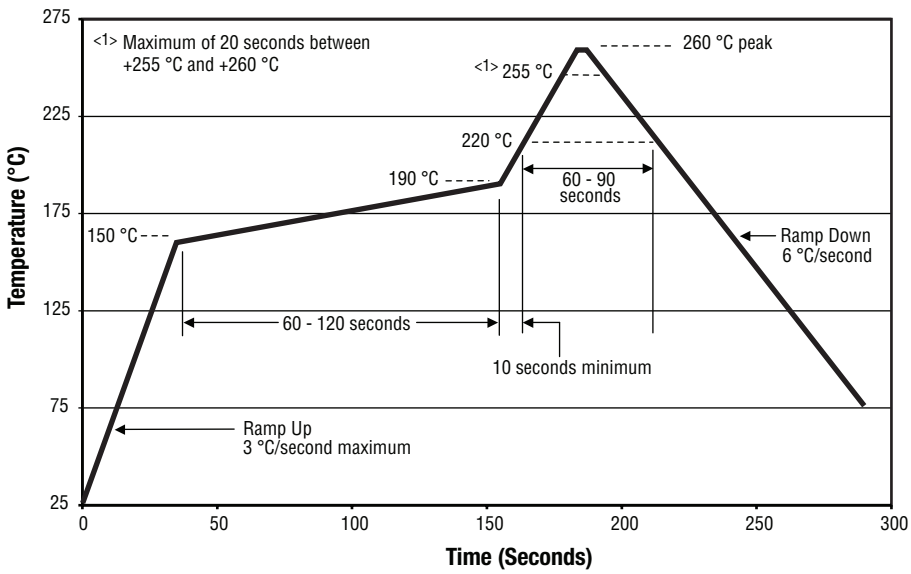
Resistance Tolerance
J = ±5 %

Number of Resistors
2 = 2 Resistors
4 = 4 Resistors

Special Characteristics
LF = Tin-plated Terminations (RoHS Compliant)

For Standard Values Used in Capacitors, Inductors, and Resistors, click here.

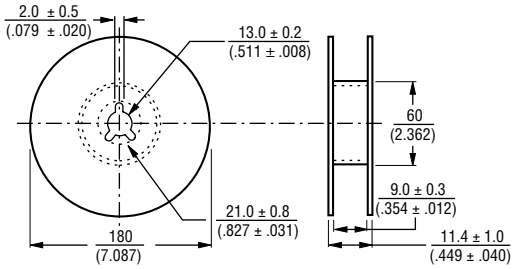
Soldering Profile

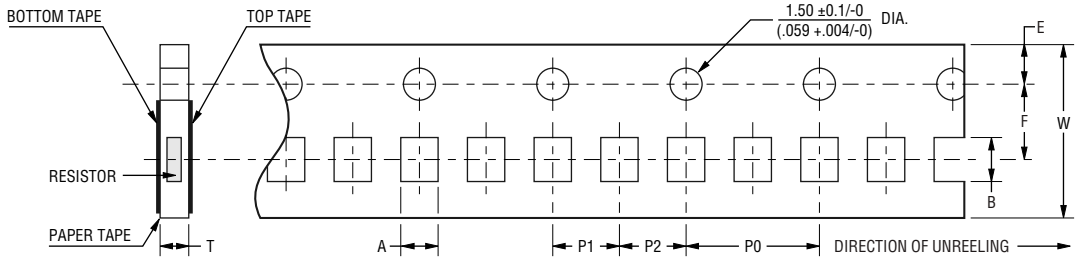


Performance Characteristics (AEC-Q200)

Test	Procedure	Test Limits
Short Time Overload	2.5 X rated voltage for 5 sec.	± (2.0 % + 0.1 Ω) 0 Ω : 50 mΩ or less
High Temperature Exposure (Storage)	1000 hrs. @ T=125 °C. Unpowered. Measurement at 24 ±2 hours after test conclusion.	5 %: ± (2.0 % + 0.1 Ω) 0 Ω: 50 mΩ or less
Temperature Cycling	1000 Cycles (-55 °C to +125 °C) Measurement at 24 ±4 hours after test conclusion. 30 min. maximum dwell time at each temperature extreme. 1 min. maximum transition time.	± (2.0 % + 0.1 Ω) 0 Ω : 50 mΩ or less
Moisture Resistance	T=24 hours / Cycle,10 Cycles. Notes: Steps 7a & 7b not required. Unpowered.	± (2.0 % + 0.1 Ω) 0 Ω : 50 mΩ or less
Biased Humidity	1000 hours 85 °C / 85 % RH. Note: Specified conditions: 10 % of operating power (not exceeding max. working voltage). Measurement at 24 ±2 hours after test conclusion.	± (3 % + 0.1 Ω) 0 Ω: 100 mΩ or less
Operational Life	1000 hours T_A=125 °C at 35 % rated power. Measurement at 24 ±4 hours after test conclusion.	± (3 % + 0.1 Ω) 0 Ω: 100 mΩ or less
Mechanical Shock	Wave Form: Tolerance for half sine shock pulse. Peak value is 100 g's. Normal duration (D) is 6 ms.	± (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less
Vibration	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Test from 10-2000 Hz.	± (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less
Resistance to Soldering Heat	Condition B: Immerse the specimens in an eutectic solder at 260 ±5 °C for 10 ±1 s.	± (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less
Thermal Shock	-55 °C / +155 °C. Note: Number of cycles required: 300, Maximum transfer time: 20 seconds, dwell time: 15 minutes. Air to Air.	± (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less
ESD	Verify the voltage setting at 500 V	± (2 % + 0.1 Ω)
Solderability	Method B, aging 4 hours at 155 °C dry heat Lead-free solder bath at 235 ±3 °C Dipping time: 3 ±0.5 seconds	> 95 % area covered with tin
Flammability	V-0 or V-1 are acceptable. Electrical test not required.	V-0 or V-1
Board Flex (Bending)	The duration of the applied forces shall be 60 (+ 5) sec.	± (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less
Terminal Strength (SMD)	Force of 1.8 kg for 60 seconds.	± (1 % + 0.05 Ω) 0 Ω: 50 mΩ or less

Packaging Dimensions





Model	A	B	W	F	E	P1	P2	P0	T
CAY10A-xxxJ2LF	1.2 ± 0.15 (.047 ± .006)	1.2 ± 0.1 (.047 ± .004)	8.0 ± 0.2 (.315 ± .008)	3.50 ± 0.05 (.138 ± .002)	1.75 ± 0.1 (.069 ± .004)	2.0 ± 0.1 (.079 ± .004)	2.0 ± 0.05 (.079 ± .002)	4.0 ± 0.1 (.157 ± .004)	0.45 ± 0.1 (.018 ± .004)
CAY10A-xxxJ4LF		2.2 ± 0.2 (.087 ± .008)							0.64 ± 0.1 (.025 ± .004)

DIMENSIONS: MM / (INCHES)

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