

### **Features**

- Lead free
- RoHS compliant\*
- Resistor ladder in 1:2 ratio
- Stable thin-film-on-silicon technology
- Ultra-miniature packages to JEDEC standards



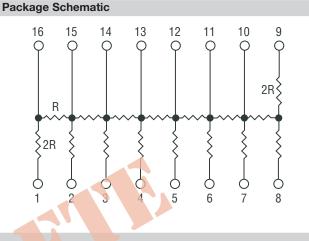
### Applications

- Digital to analog converters
- Successive approximation ADCs
- Ideal for space-constrained applications

## Thin Film on Silicon 2QSP / 2NBS-XX6 R2R Ladder

### **General Information**

The R2R Ladder Network is used in Digital to Analog and Analog to Digital conversion. Binary weighted currents, flowing in the individual ladder segments, depend on the integrity of the R:2R relationship for an accurate conversion result. Fabricated with Tantalum Nitride on Silicon, these resistors feature excellent stability, TCR and tracking performance. R2R Ladder Networks are available in a range of miniature packages conforming to JEDEC standards.



#### **Electrical & Environmental Characteristics**

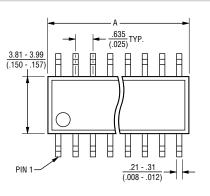
Electrical Characteristics	Symbol	Minimum	Nominal	Maximum	Unit
Resistance Range	R	10		50 K	Ω
Tolerance: Absolute Ratio		±1 % ±0.5 %		±5 %	Ω Ω
TCR: Absolute Tracking			100 25		ppm/°C ppm/°C
Operating Voltage				50	V
Environmental Characteristics		2 K			v
Operating Temperature	ТЈ	-55		+125	°C
Storage Temperature	T <sub>stg</sub>	-65		+150	°C
Power Rating per Resistor @ 70 °C				0.1	Watt
Power Rating per Package @ 70 °C: QSOP: 16 Pin 20, 24 Pin 28 Pin				0.75 1.00 1.12	Watt Watt Watt
NBSOIC: 8 Pin 14, 16 Pin				0.60 1.00	Watt Watt

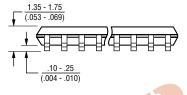
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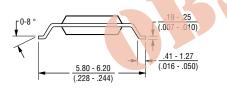
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#### **Mechanical Characteristics**

#### **QSOP** Package Dimensions





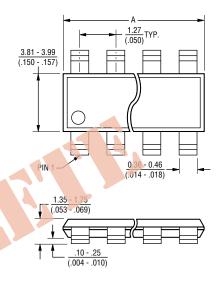


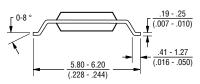
Model	Α			
2QSP16	4.80 - 4.98 (.189196)			
2QSP20	8.56 - 8.74 (.337344)			
2QSP24	8.56 - 8.74 (.337344)			
2QSP28	9.80 - 9.98 (.386393)			

Governing dimensions are in mm. Dimensions in parentheses are in inches and are approximate.

JEDEC Reference Number MO-137.

### Narrow-Body SOIC Package Dimensions





Model	Α			
2NBS08	4.80 - 4.98 (.189196)			
2NBS14	8.56 - 8.74 (.337344)			
2NBS16	9.80 - 9.98 (.386393)			

Governing dimensions are in mm. Dimensions in parentheses are in inches and are approximate.

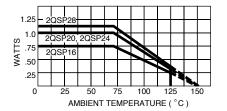
JEDEC Reference Number MS-012.

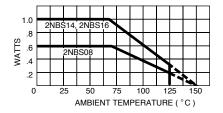
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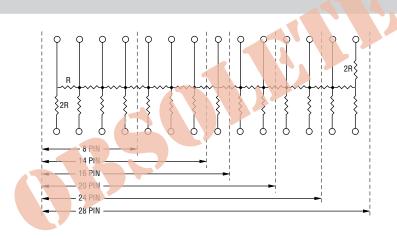
### QSOP Package Power Temperature Derating Curve

### Narrow-Body SOIC Package Power Temperature Derating Curve



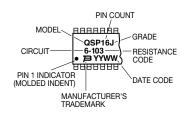


### Schematic



### **Typical Part Marking**

Represents total content. Layout may vary.



### **Standard Resistance Values**

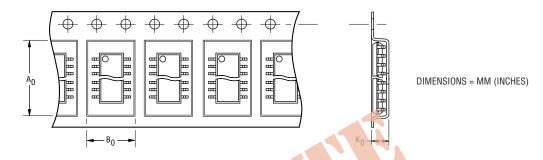
R1 Value (ohms)	R2 Value (ohms)	Resistance Code
10 K	20 K	103
25 K	50 K	253

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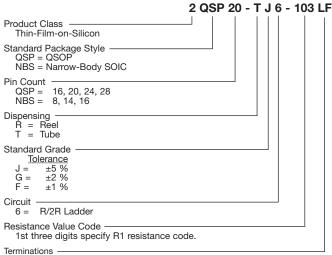
#### Dispensing

For large quantities, the product will be dispensed in Tape and Reel (see diagram below).



Package	A <sub>0</sub>	B <sub>0</sub>	K <sub>0</sub>	Width	Pitch	No. of Pieces per 13 reel	No. of Pieces per tube
QSOP							
16 Pin	6.4 (0.252)	5.2 (0.205)	2.1 (0.083)	12 (0.472)	8 (0.315)	3,500	98
20, 24 Pin	6.5 (0.256)	9.0 (0.354)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	56
28 Pin	6.5 (0.256)	10.3 (0.406)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	49
NBSOIC							
8 Pin	6.4 (0.252)	9.0 (0.354)	2.1 (0.083)	12 (0.472)	8 (0.315)	3,500	98
14 Pin	6.5 (0.256)	9.0 (0.354)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	56
16 Pin	6.5 (0.256)	9.0 (0.354)	2.1 (0.083)	16 (0.630)	8 (0.315)	3,500	49

### How To Order



• LF = 100 % Sn (lead free)



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